



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22304-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7594

SERIAL NUMBER 10/759,462	FILING DATE 01/16/2004 RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. 333772000420
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Rochit Rajsuman, Santa Clara, CA;
Robert Sauer, Santa Clara, CA;
Hiroki Yamoto, Santa Clara, CA;

**** CONTINUING DATA** *yes pme*
This application is a CIP of 10/340,349 01/10/2003
which is a CIP of 09/547,752 04/12/2000 PAT 6,567,941

**** FOREIGN APPLICATIONS** *none pme*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
**** 04/23/2004**

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 13	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>pme</i>	INITIALS		

ADDRESS
25224
MORRISON & FOERSTER, LLP
555 WEST FIFTH STREET
SUITE 3500
LOS ANGELES, CA
90013-1024

TITLE
Semiconductor test system storing pin calibration data, commands and other data in non-volatile memory

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
------------	---	---